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**Complete If Known**

Application Number 10/716,006

Filing Date 11/18/2003

First Named Inventor Mehregany

Art Unit 1775

Examiner Name Archene A. Turner

Attorney Docket Number 29438/04001

Sheet


2

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2

**NON PATENT LITERATURE DOCUMENTS**

Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
A		Hwang, J-D et al, "Epitaxial Growth and Electrical Characteristics of Beta-SiC on Si by Low-Pressure Rapid Thermal Chemical Vapor Deposition." Japanese Journal of Applied Physics. Tokyo, JP, vol. 34, no. 3, Part 1, March 1995 (1995-03), pgs. 1447-1450. XP-000703002	
		Sarro, P. M. et al., "Low-Stress PECVD SiC thin films for IC-compatible microstructures." Sensors and Actuators A, Elsevier Sequoia S.A., Lausanne, CH, vol. 67, no. 1-3, May 15, 1998, pgs. 175-180.	
A		Search Report for PCT/US2004/037064, published 9/12/2005.	

Examiner Signature		Date Considered	12/05
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\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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